

Application No.: 10/707,536

Docket No.: 22040-00023-US

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Cancelled).

2. (Currently amended) ~~The integrated circuit according to claim 1,~~ An integrated circuit that integrates a plurality of functional blocks within a single semiconductor chip, the integrated circuit comprising:

a plurality of decoding circuits within said semiconductor chip, for which address lines and data lines are connected to an input terminal, an address signal inputted from said address lines is decoded, and the data inputted from said data lines is outputted according to the results of the decoding; and

signal lines with a few bits including said address lines and said data lines, wherein said signal lines are wired to said plurality of the decoding circuits.

wherein said plurality of the decoding circuits are arranged in the same number of said plurality of functional blocks.

3. (Original) The integrated circuit according to claim 2, wherein said plurality of the decoding circuits are respectively arranged near said plurality of functional blocks.